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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE - EXAMINING GROUP 2800

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Box AF, Commissioner of Patents, Washington, DC 20231.

Date

August 23, 2002

Denise Sheridan

Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Pierre C. Fazan and
Gurtej S. Sandhu

Attorney Docket No.: 500055.02 (660073.488D1)

Serial No. : 09/386,646

Group Art Unit : 2811

Filed : August 31, 1999

Examiner : H. Vu

Title : METHOD AND APPARATUS FOR TRENCH ISOLATION PROCESS WITH PAD
GATE AND TRENCH EDGE SPACER ELIMINATION

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RESPONSE UNDER 37 C.F.R. § 1.116

Sir:

Applicants acknowledge receipt of the Office Action: Final Rejection dated July 1, 2002.

In the Claims:

Please amend claims 22, 24-26, 28, 30, 32 and 34, and add new claims 38-43 as follows:

*[Handwritten note: 22. (Five Times Amended) A microelectronic device, comprising:
a microelectronic substrate having an upper surface;*